

*JSW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

Sreenivas et al.

Serial No.: 10/764,422

Filed: January 23, 2004

Atty. File No.: 1604-409-DIV

For: DUAL BAND COPLANAR  
MICROSTRIP INTERLACED ARRAY

Group Art Unit: 2821

Examiner:

REQUEST FOR CORRECTION OF  
FILING RECEIPT

Office of Initial Patent Examinations  
Customer Service Center  
Commissioner for Patents  
P.O. Box 1450  
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<p align="center">CERTIFICATE OF MAILING</p> <p>I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO THE COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 ON <u>5/25/04</u></p> <p align="center">SHERIDAN ROSS P.C.</p> <p>BY: <i>Christine Jacques</i></p>
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Dear Sir:

The official Filing Receipt for the above-referenced patent application contains the following clerical errors:

The total number of claims is 45 and not 5. The total number of independent claims is 4 and not 1. A check in the amount of \$1,306.00 was submitted with the filing of the application and not \$770.00 as indicated on the filing receipt.

For your reference, a copy of the Filing Receipt is enclosed, with the corrections indicated in red. Please issue a corrected Filing Receipt for this patent application.

Respectfully submitted,

SHERIDAN ROSS P.C.

By:

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Date: May 25, 2004



## UNITED STATES PATENT AND TRADEMARK OFFICE

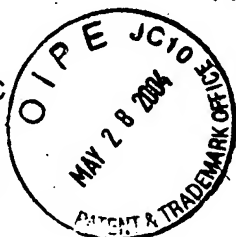
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/764,422	01/23/2004	2821	<del>770</del> 1306	1604-409-DIV	18	<del>5</del> 45	<del>1</del> 4

CONFIRMATION NO. 8973

22442  
 SHERIDAN ROSS PC  
 1560 BROADWAY  
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 DENVER, CO 80202



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SHERIDAN, ROSS

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\*OC000000012502225\*

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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Ajay I. Sreenivas, Niwot, CO;  
 Farzin Lalezari, Boulder, CO;

## Domestic Priority data as claimed by applicant

This application is a DIV of 10/056,413 01/24/2002

## Foreign Applications

If Required, Foreign Filing License Granted: 04/30/2004

Projected Publication Date: 08/12/2004

Non-Publication Request: No

Early Publication Request: No

## Title

Dual band coplanar microstrip interlaced array

## Preliminary Class

343

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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